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two cycle faulty address compare

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[PDF] IBM z990 Soft Error Detection and Recovery

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reject scheme is also used for **address compare** and other cache coherent protocols. ... Superscalar replication with a **two-cycle** delay. ...

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[PDF] A 7ns cycle, high speed dual compare CAM in 0.6/spl mu/m GaAs ...

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sive suite of test vectors to provide adequate **fault** coverage. ... hit **address**. generation time. is. 911s. one clock **cycle** for the ...

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[PDF] z990 methods FINAL PDF.doc

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deleted (since they indicate the cache is not necessarily at **fault**). For ... also used for **address compare** and other cache coherent protocols. ...

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These **two** methods were selected. for **comparison** because the execution of the **fault** injection. experiments is identical. In the following text, we will refer ...

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[PDF] ISSCC 2001 / SESSION 2 / NON-VOLATILE MEMORIES / 2.4

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gram **cycle** can now be resumed. Embedded systems usually ... quick **comparison** between an applied **address** and the data. stored in the CAM. ...

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[PDF] A fault-tolerant associative memory with high-speed operation ...

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ble to **compare** and read/write during the the same clock. **cycle**. This facility was obtained ... memory array is divided into **two** blocks with the **address** ...

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[PDF] Testing Cpu Based Boards For Functionality Using Bus Cycle ...

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address. But again if the kernel is **faulty** it may be. writing the code some where else or ... tures in **two** major modes. a) At Bus **Cycle** count signature mode ...

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[PDF] A software methodology for detecting hardware faults in VLIW data ...

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design methodology for achieving on-line hardware **fault** detection capabilities in VLIW ... defined as consisting of **two** instructions: **comparison** between the ...

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[PDF] **A comparison of North American (ANSI) and European (IEC) fault ...**

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to analyze linear networks and still properly **address** these. concerns. ... rents that are customarily computed at half **cycle** after **fault**. initiation. ...

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[PDF] **Transient fault detection via simultaneous multithreading ...**

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same program on **two** identical. lockstepped. (**cycle**-synchronized). microprocessors. ... this entry initialized, so it will **compare** its **address** and data with ...

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